FORM SB/08HDP-1449 (Based on Form PTO-1449)

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U.S. PATENT DOCUMENTS

Ref. Desig.	Examiner's Initials	Document Number	Date	Name	Class/ Subclass	(If appropriate) Filing Date
1.		6,606,489 B2	8/2003	Razavi et al	—	
2.		6,870,881	3/2005	He, Runsheng	—	

FOREIGN PATENT DOCUMENTS

Ref. Desig.	Examiner's Initials	Document Number	Date	Country	Class/ Subclass	Translation Yes	No
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OTHER DOCUMENTS (including Author, Title, Date, Pertinent Pages, etc.)

Ref. Desig.	Examiner's Initials	
1.		Stonick et al; "An Adaptive PAM-4 5-Gb/s Backplane Transceiver in 0.25-um CMOS; IEEE Journal of Solid-State Circuits, Vol. 38, No. 3, March 2003; pp. 436-443

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U.S. PATENT DOCUMENTS

Ref. Desig.	Examiner's Initials	Document Number	Date	Name	Class/ Subclass	(If appropriate) Filing Date
1.	CJ	6,975,674	12/2005	Phanse et al.	375/219	

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FORM SDP-1449 (Based on Form PTO-1449)

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U.S. PATENT DOCUMENTS

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3.		6,606,489 B2	08/2003	Razavi et al.	—	
4.		6,744,931	06/2004	Komiya et al.	—	
5.		6,870,881	03/2005	He, Runsheng	—	
6.		6,975,674	12/2005	Phanse et al.	375/219	

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1.		Azadet, Kamran and Nicole, Chris; "Low-Power Equalizer Architectures for High-Speed Modems"; October 1998; pages 118-126
2.		Chien et al; "TP 12.4: A 900-MHz Local Oscillator using a DLL-based Frequency Multiplier Technique for PCS Applications"; Journal of IEEE Solid State Circuits; Feb. 2000; pgs. 202-203 and 458
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50.	-4	Stonick et al; "An Adaptive PAM-4 5-Gb/s Backplane Transceiver in 0.25-um CMOS; IEEE Journal of Solid State Circuits, Vol. 38, No. 3, March 2003; pp. 436-443.

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